

IN THE DRAWINGS:

The attached sheet of drawings includes changes to Figure 4 and Figure 5.

Attachment: Replacement Sheets
Annotated Sheets Showing Changes

REMARKS

This is intended as a full and complete response to the Office Action dated March 8, 2005, having a shortened statutory period for response set to expire on June 8, 2005. Please reconsider the claims pending in the application for reasons discussed below.

In the specification, paragraph [0030] has been amended to correct minor editorial problems.

Claims 1-24 are pending in the application. Claims 1, 6, 7, 20, and 21 have been cancelled. Claims 2-5, 8-19, and 22-24 remain pending following entry of this response. Applicant submits that amendments presented herein do not introduce new matter.

Drawing Objections

The drawings are objected to because labels in the drawings are not consistent with the specification. Replacement sheets, submitted herewith, have revised the labels to be consistent with the specification. Accordingly, Applicant respectfully requests withdrawal of this objection.

Claim Rejections - 35 USC § 102

Claims 1, 6, 7, 20, and 21 are rejected under 35 U.S.C. 102(e) as being anticipated by Kiehl (US 6,492,836 B2). These claims have been cancelled, thus obviating this rejection.

Allowable Subject Matter

Claims 13-19 are indicated to be allowable over the prior art.

Claims 2-5, 8-12, and 22-24 are objected to as being dependent upon rejected base claims, but would be allowable if rewritten in independent form including all the limitations of the corresponding base claims. Applicant has rewritten these claims in independent form, as suggested. Accordingly, Applicant submits these claims are allowable and respectfully requests withdrawal of this rejection.

Conclusion

Having addressed all issues set out in the office action, Applicant respectfully submits that the claims are in condition for allowance and respectfully requests that the claims be allowed.

Respectfully submitted,



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The diagram shows a circuit with two main parts. The top part, labeled 401, is enclosed in a dashed box and contains two sub-circuits: a 'DIFFERENTIAL AMPLIFIER STAGE' (410) and an 'INVERTER STAGE' (420). The differential amplifier stage 410 has two input nodes, both labeled V_{IN} . One node is connected to a V_{REF} source and an 'ENABLE' signal. The other node is connected to a V_{DD} source. The inverter stage 420 consists of a PMOS network with transistors MP1 and MP2, and an NMOS network with transistors MN1 and MN2. Currents I_{P1} , I_{CN} , I_{N1} , and I_{CP} are indicated. The output of the inverter stage is V_{OUT} . The bottom part of the diagram shows a current source circuit. It includes a PMOS transistor MPC1 and an NMOS transistor MNC1, with a handwritten note I_{CN}' and a crossed-out I_{CN} . Another PMOS transistor MPC2 and NMOS transistor MNC2 are shown, with a handwritten note I_{CP}' and a crossed-out I_{CP} . The circuit is powered by V_{DD} and V_{SS} (ground).

ANNOTATED SHEET SHOWING CHANGES

ATTY DKT. NO.: INFN/SZ0028
 U.S. SERIAL NO.: 10/716,079
 FILED: NOVEMBER 18, 2003
 TITLE: LOW RISE/FALL SKEWED INPUT BUFFER COMPENSATING
 PROCESS VARIATION
 INVENTOR(S): JONGHEE HAN

CONF. NO.: 4850

5/5

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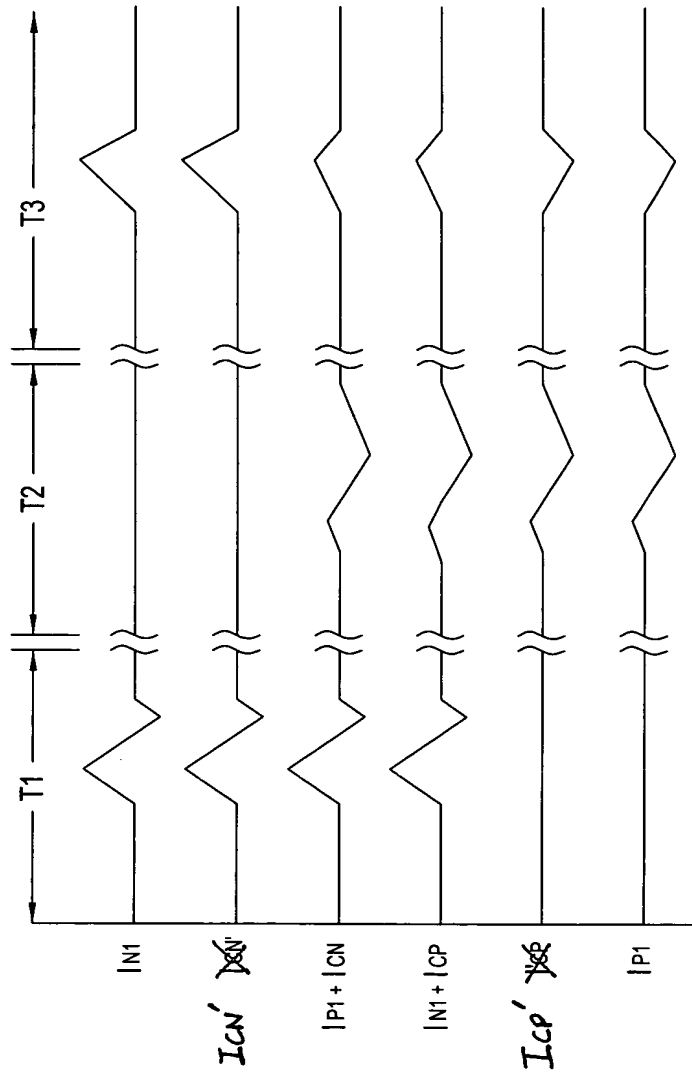


FIG. 5